

Advanced Communication Lab											
Time/ Day	Slot 1	Slot 2	10.35- 10.45am	Slot 3	Slot 4	12.25pm - 01.15 pm	Slot 5	Slot 6	3.00- 3.10pm	Slot 7	Slot 8
	08.55- 09.45a m	09.45- 10.35a m		10.45- 11.35a m	11.35- 12.25p m		01.20- 02.10p m	02.10- 03.00p m		03.10- 04.00p m	04.00- 04.50p m
Monday	VLSI - VI CCE			23CCE183 Signal Processing Laboratory - II CCE				22AIE113 ECS 2 - AIE A			
Tuesday							23CCE387 SDR Lab B1				
Wednesday				22AIE113 ECS 2- II AIE C				22AIE113 ECS 2 - AIE B			
Thursday				22AIE113 ECS 2- II AIE C				23CCE385 Open Lab II			
Friday	23CCE387 SDR Lab B2						23CCE385 Open Lab II				